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PATENT
5500-88800/TT5375

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/676,437

Filed: October 1, 2003

Inventors:

Mitchell Alsup

Gregory William Smaus

James K. Pickett

Brian D. McMinn

Michael A. Filippo

Benjamin T. Sander

Title: System and Method for
Handling Exceptional
Instructions in a Trace
Cache Based Processor

§ Examiner: Kim, Hong Chong
§ Group/Art Unit: 2185
§ Atty. Dkt. No: 5500-88800

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Printed Name

May 15, 2006

Signature

Date

RESPONSE TO OFFICE ACTION OF FEBRUARY 14, 2006

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of February 14, 2006, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.